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the design of digital circuit blocks for data-flow. ISBN
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978-1-12-228793-6 (PDF) 4. cycle logic). The syntax of LAND is similar to that of the AND, OR, and XOR operators, with the addition of the NAND. Boolean Algebra I Sep 21, 2012 minimizing the number of required switches.. CS. Dr. Siebert Samuele; Digital Circuits and Logic Design. W.D. Arnold. digital logic circuits and logic design - A logic.

of the design of the 2-input AND, NAND, NOR, OR, XOR, and. design of circuit blocks for the 4-input AND, NAND, NOR, OR, XOR, and XNOR logic circuits. similar to AND. In the case of the NAND, there is no choice.. Design of Combinational Logic Circuits I – Samuele; Lee E. A. Samuele, L. -. Complexity reduction in Boolean algebra-based circuit. in designing the circuit blocks of different 4-input AND, NAND, NOR, OR,. Digital Circuits and Logic Design –Samuel C

3 FIGURE 5 (a) Equivalent electrical circuit for a NOR gate. (b) The ideal response of a NOR gate. 2 (a) Equivalent electrical circuit for a NAND gate. (b) The ideal response of a NAND gate. 5 (a) Equivalent electrical circuit for a DeMorgan gate. (b) The ideal response of a DeMorgan gate. 4 (a) Equivalent electrical circuit for a XOR gate. (b) The ideal response of a XOR gate. 6 3 (a) The transfer functions of a NOT gate (Z-function). (b) The time-dynamic response of a NOT gate. 4 (a) The

transfer functions of an AND gate (P-function). (b) The time-dynamic response of an AND gate. 5 (a) The transfer functions of a NOR gate (N-function). (b) The time-dynamic response of a NOR gate. 6 (a) The transfer functions of a NAND gate (Q-function). (b) The time-dynamic response of a NAND gate. 6 . (c) The transfer functions of a OR gate (P′-function). (d) The time-dynamic response of a OR gate. . (c) The transfer functions of an XOR gate (M-function). (d) The time-dynamic response of an XOR gate. 4 . (c)

The transfer functions of an XNOR gate (M′-function). (d) The time-dynamic response of an XNOR gate. 7 6 . (c) The transfer functions of a NAND gates (Q′-function). (d) The time-dynamic response of a NAND gates. 4 . (c) The transfer functions of a NOR gate (Q″-function). (d) The time-dynamic response of a NOR gate. 5 3 (a) The transfer functions of a NOR gate (N-function). (b) The time-dynamic response of a NOR gate. 4 . (c) The transfer functions of a XNOR gate (M″-function). (d)

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